

**RESPONSE AFTER FINAL  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2816****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

MATSUZAKI

Art Unit: 2816

Application No. 09/874,037

Examiner: H. Nguyen

Filed: June 6, 2001

Atty. Docket No. 100353-00064

For: A SEMICONDUCTOR PHASE ADJUSTMENT SYSTEM MODULE

**AMENDMENT UNDER 37 C.F.R. § 1.116**

Commissioner for Patents  
Washington, D.C. 20231

January 7, 2003

Sir:

In reply to the Office Action mailed October 7, 2002, please amend the above-identified application as follows:

**IN THE CLAIMS:**

Please cancel claims 1 and 3 without prejudice.

Please amend claims 2, 4, 5, 15-23 and 25 as follows:

2. (Amended) A module comprising:

a semiconductor device;

a phase adjustment circuit which receives a phase adjustment signal output from said semiconductor device and a first clock supplied from an exterior of said module, and generates a second clock; and

an output circuit that is provided in the semiconductor device and generates the phase adjustment signal from the second clock, wherein said phase adjustment circuit